



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Allan Robert Knoll, et al.

Title: NOVEL SUPERCONDUCTING ARTICLES, AND METHODS FOR FORMING AND USING SAME

Application No.: 10/750,144

Filed: December 31, 2003

Examiner: UNASSIGNED

Group Art Unit: 2831

Atty. Docket No.: 1014-SP230

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
 Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance, such as through (i) an English language abstract, (ii) an English language equivalent application, (iii) reference to discussion in the application, or (iv) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that

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Elise K. Dougherty
Typed or Printed Name

E.K. Dougherty
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indicates the degree of relevance found by the foreign office.

STATEMENT UNDER 37 C.F.R. § 1.704(d)

If the above-identified application is an original application filed on or after May 29, 2000:

each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

FEES DUE

This Information Disclosure Statement is being filed:

within three months of the filing date of a national application or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

during the period specified in § 1.97(c). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.

during the period specified in § 1.97(c). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal.

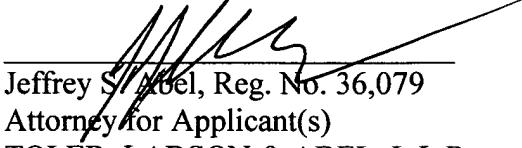
during the period specified in § 1.97(d). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal. Additionally, each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.

Should any PTO fees be necessary for entry of this Information Disclosure Statement, the undersigned hereby authorizes the Commissioner to charge Deposit Account 50-2469.

Date

4/20/05

Respectfully submitted,


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PTO/SB/08A (08-03)

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of 2

Complete if Known

Application Number	10/750,144
Filing Date	12/31/2003
First Named Inventor	Allan Robert Knoll
Art Unit	2831
Examiner Name	UNASSIGNED
Attorney Docket Number	1014-SP230

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number <small>Number-Kind Code² (if known)</small>	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A	US-	5,527,766	06/18/1996	Eddy	
B	US-	5,650,377	07/22/1997	Kern et al.	
C	US-	6,388,268 B1	05/14/2002	Kim et al.	
D	US-				
E	US-				
F	US-				
G	US-				
H	US-				
I	US-				
J	US-				
K	US-				
L	US-				
M	US-				
N	US-				
O	US-				
P	US-				
Q	US-				
R	US-				
S	US-				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document <small>Country Code³-Number⁴-Kind Code⁵ (if known)</small>	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
T	JP 3-283679		12/13/1991	Shimadzu Corp.	English Abstract	<input type="checkbox"/>
U	JP 6-85337		03/25/1994	Fuji Electric Co., Ltd.	English Abstract	<input type="checkbox"/>
V	JP 2000-261053		09/22/2000	Fujitsu Ltd.	English Abstract	<input checked="" type="checkbox"/>
W	JP 2001-168404		06/22/2001	Samsung Elect. Co., Ltd.	English Abstract	<input type="checkbox"/>
X	WO 01/18885 A1		03/15/2001	Cambridge U. Tech Serv.		<input type="checkbox"/>
Y						<input type="checkbox"/>

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First Named Inventor	Allan Robert Knoll
Art Unit	2831
Examiner Name	UNASSIGNED
Attorney Docket Number	1014-SP230

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	Pedarnig, J.D., et al., "Patterning of YBa ₂ Cu ₃ O ₇ - Films using a Near-Field Optical Configuration", APPL. PHYS. A., Vol. 67, pp. 403-405, 1998	
	BB	Cobb, Coleman B., et al., "Hysteretic Loss Reduction in Striated YBCO", Physica C., Vol. 382, pp. 52-56, 2002.	
	BC	Carr, Jr., W.J., et al., "Filamentary YBCO Conductors for AC Applications", IEEE Transactions on Applied Superconductivity, Vol. 9, No. 2, pgs 1475-1478, June 1999.	
	BD	Sumption, M.D., et al., "Hysteretic Loss vs. Filament Width in Thin YBCO Films Near the Penetration Field, IEEE Transactions on Applied Superconductivity, Vol. 13., No. 2, pgs 3553-3556, June 2003.	
	BE	Suzuki, Y., "Transport Properties of Patterned Thin Lines From Epitaxial YBCO", Cornell Nanofabrication Facility, National Nanofabrication Users Network, pgs. 178-179.	
	BF	Meltaus, J., et al., "High Temperature Superconducting Thin-Film Patterning for Microwave Applications", Materials Physics Laboratory, Helsinki University of Technology, 1 pg.	
	BG	Hakola, A., "Patterning of HTS Thin Films", Department of Engineering Physics and Mathematics, Helsinki University of Technology, http://www.hut.fi/Units/AES/projects/prlaser/patterning.htm , 2 pgs.	
	BH	"State-of-the-art of the HTS Microfabrication Technology", http://www.imel.demokritos.gr/web/NATO_973718/Proposal/stateofheart_ofHTStechnol... , 4 pgs, 07/17/2003.	
	BI	Tralshawala, N., et al., "Session K20 - Industrial Applications of High-Temperature Superconductor Materials", Mixed Session, Wednesday afternoon, March 19, 5 pgs.	
	BJ		

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